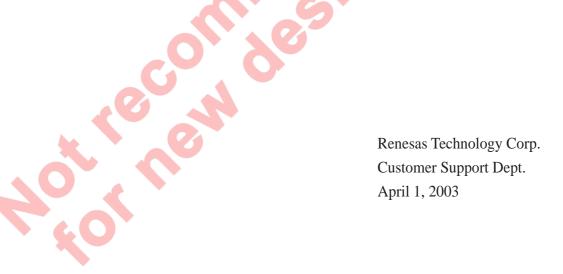
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2SD1490

Silicon NPN Epitaxial

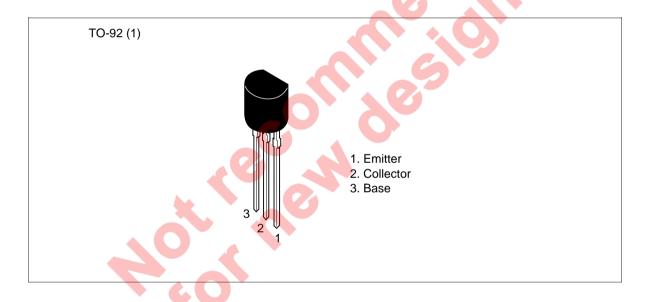


ADE-208-1157A (Z) 2nd. Edition Mar. 2001

Application

• Low frequency power amplifier

Outline



2SD1490

Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	70	V
Collector to emitter voltage	V_{CEO}	50	V
Emitter to base voltage	V_{EBO}	6	V
Collector current	I _c	1	A
Collector power dissipation	P _c	0.75	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

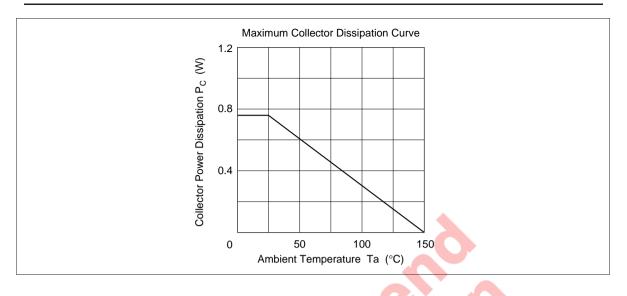
Electrical Characteristics ($Ta = 25^{\circ}C$)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(BR)CBO}$	70	_	1	V	$I_C = 10 \ \mu A, \ I_E = 0$
Collector to emitter breakdown voltage	$V_{(BR)CEO}$	50	7	7	V	$I_C = 1 \text{ mA}, R_{BE} = \infty$
Emitter to base breakdown voltage	$V_{(BR)EBO}$	6		X	V	$I_{E} = 10 \ \mu\text{A}, \ I_{C} = 0$
Collector cutoff current	I _{CBO}	-	_ ,	1	μΑ	$V_{CB} = 80 \text{ V}, I_{E} = 0$
Emitter cutoff current	I _{EBO}		-	0.2	μΑ	$V_{EB} = 6 \text{ V}, I_{C} = 0$
DC current transfer ratio	h _{FE} *1	100	47	500		$V_{CE} = 2 \text{ V}, I_{C} = 0.1 \text{ A}$
Collector to emitter saturation voltage	V _{CE(sat)}	-6	-	0.3	V	I _C = 1 A, I _B = 0.1 A
Gain bandwidth product	f _⊤	7	80	_	MHz	$V_{CE} = 2 \text{ V}, I_{C} = 10 \text{ mA}$
Collector output capacitance	Cob		20	_	pF	$V_{CB} = 10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$

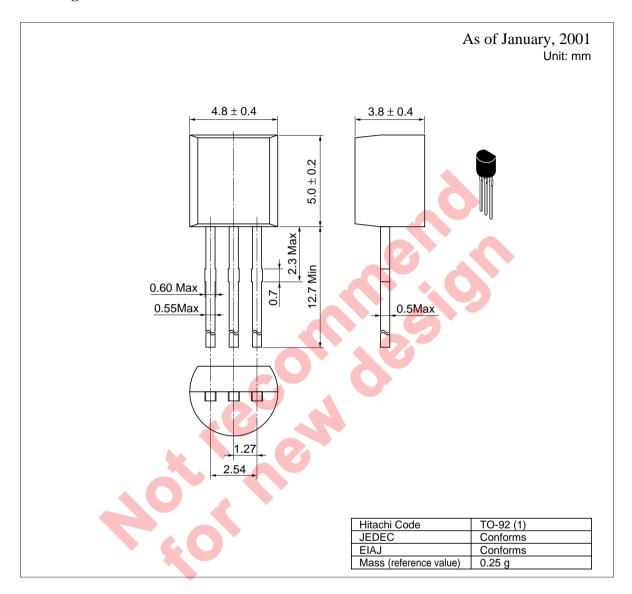
Note: 1. The 2SD1490 is grouped by h_{FE} as follows.

В	C	D
100 to 200	160 to 320	250 to 500

See characteristic curves of 2SD789.



Package Dimensions



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